

1

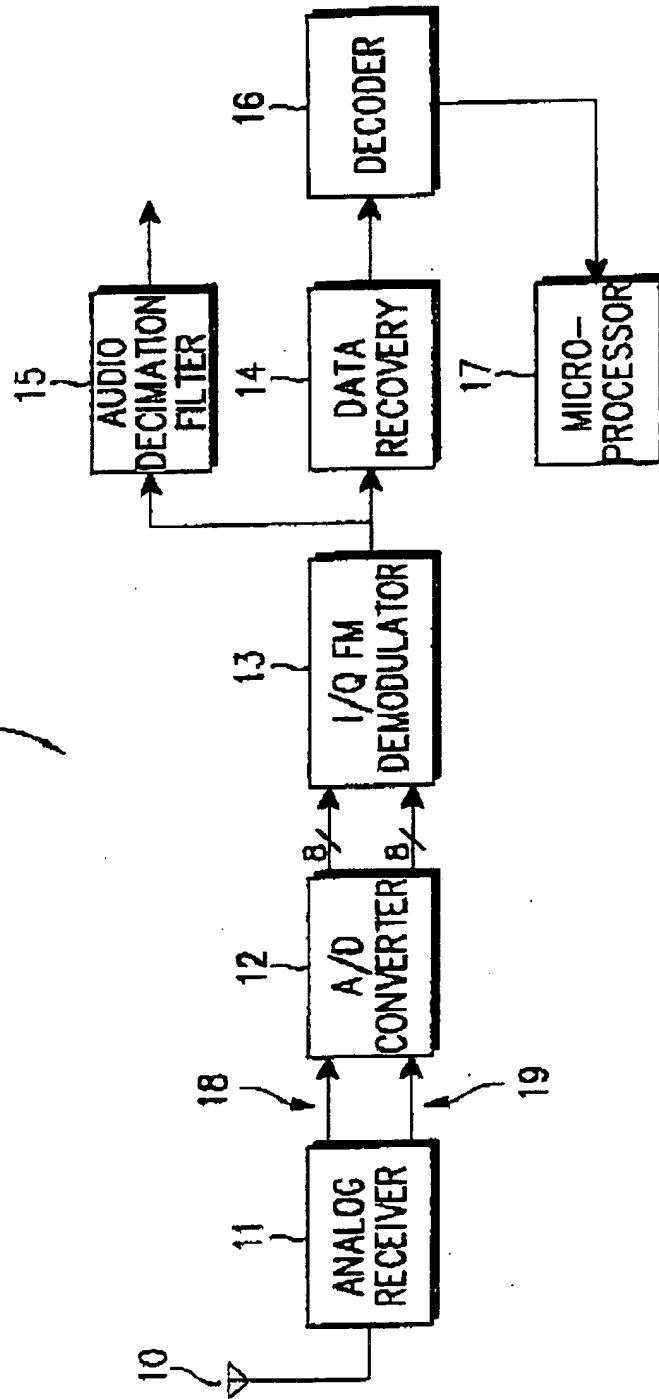


FIG.1  
(PRIOR ART)



2/7

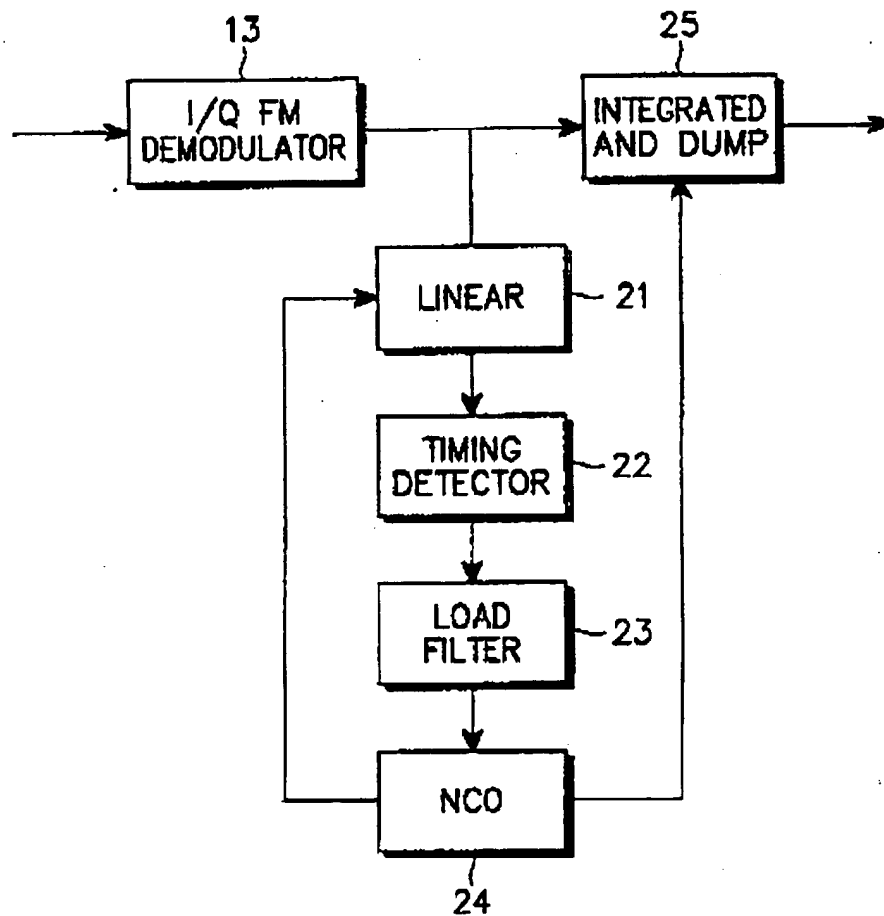
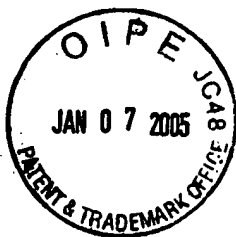


FIG.2  
(PRIOR ART)



3/7

DOTTING SEQUENCE		MESSAGE WORD		DOTTING SEQUENCE		MESSAGE WORD		DOTTING SEQUENCE		MESSAGE WORD		DOTTING SEQUENCE		MESSAGE WORD	
SIGNAL 1		SIGNAL 1		SIGNAL 2		SIGNAL 2		SIGNAL 1		SIGNAL 1		SIGNAL 1		SIGNAL 1	
(101 BIT)		(11 BIT)		(40 BIT)		(37 BIT)		(11 BIT)		(40 BIT)		(37 BIT)		(11 BIT)	

DOTTING SEQUENCE SIGNAL: 10101010~  
WORD SYNC SIGNAL: 111000110010  
MESSAGE WORD BLOCK: BCH CODED 28BIT MESSAGE STRING

FIG.3  
(PRIOR ART)

Diagram illustrating the structure of a frame and the timing of its reception:

The frame structure is shown as a sequence of fields: **Dott**, **WS**, **MESS-AGE**, **Dott**, **WS**, **MESS-AGE**, **Dott**, **WS**, **MESS-AGE**.

The reception process is indicated by a horizontal arrow pointing from right to left, labeled **←**. The text **(DETECTION TIME OF DOTTING SEQUENCE SIGNAL)** is associated with the arrow.

The reception process is summarized by the following points:

- THE END OF FRAME RECEPTION
- TIMER END

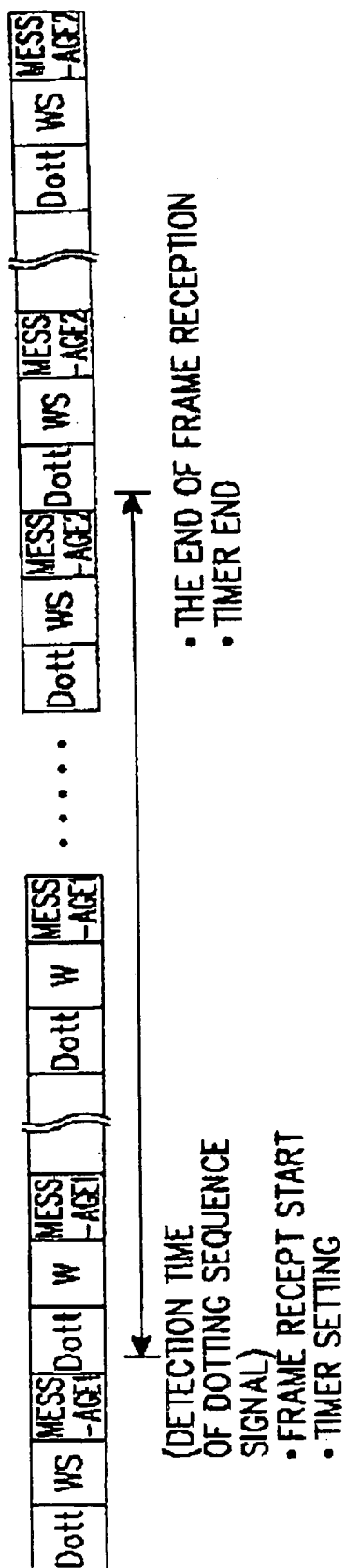


FIG.5  
(PRIOR ART)